

## An accurate large-signal model for a high-efficient Si bipolar GSM power transistor

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We present an accurate large-signal modelling approach for Si bipolar RF power transistors. An equivalent circuit model of a state of the art GSM power transistor (900 MHz, 3.5 V, 4 W, 70%) is constructed. The model is verified with accurate load pull measurements. Large-signal parameters ( $P_{\text{out}}$ ,  $G_T$ , PAE) are predicted accurately.

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